Drawing No.: RMCU-1K-HTS-0001/ 1

Date: 2025. 7. 29

Data sheet

Title: FIXED THICK FILM CHIP RESISTORS;

RECTANGULAR TYPE

Style: RMCU16, 20

AEC-Q200 qualified

RoHS COMPLIANCE ITEM Halogen and Antimony Free

Note: •Stock conditions

Temperature: $+5^{\circ}\text{C} \sim +35^{\circ}\text{C}$ Relative humidity: $25\% \sim 75\%$

The period of guarantee: Within 2 year from shipment by the company.

Solderability shall be satisfied.

 Product specification contained in this data sheet are subject to change at any time without notice

•If you have any questions or a Purchasing Specification for any quality agreement is necessary, please contact our sales staff.



Hokkaido Research Center Approval by: H. Seino Drawing by: M. Shibuya

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1. Scope

1.1 This specification covers the detail requirements for fixed thick film chip resistors; rectangular type, style of RMCU16,20.

1.2 Applicable documents

JIS C 5201-1: 2011, IEC60115-1: 2008, AEC-Q200 Rev.D

2. Classification

Type designation shall be the following form.

(Example)

1 Fixed thick film chip resistors; rectangular type

2 Rated dissipation and / or dimension

3 Temperature coefficient of resistance

K	±100×10 ⁻⁶ / °C	
–(Dash)	Standard	

4 Rated resistance

123	E24 Series, 3 digit,	Ex. 123> 12kΩ,
1000	E96 Series, 4 digit,	Ex. 1000>100Ω
		1022> 10.2kΩ

5 Tolerance on rated resistance

D	±0.5%
F	±1%
J	±5%

6 Packaging form 1. Scope

IP I Paper taping	TP	Paper taping
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3. Rating

The ratings shall be in accordance with Table-1.

3.1 Resistor

Table-1

	Table 1					
Style	Rated dissipation (W)		re coefficient of ce (10 ⁻⁶ / °C)	Rated resistance range (Ω)	Preferred number series for resistors	Tolerance on rated resistance
		K	±100	10~1M		D(±0.5%)
			±200	1.02M~10M	E34.06	
RMCU16	0.125	K	±100	10~1M	E24, 96	F(±1%)
		Standard	±200	1.0~9.76		
		Standard	±200	1~22M	E24	J(±5%)
		K	±100	10~1M		D(±0.5%)
		Standard	±200	1.02M~10M	E04.06	
RMCU20	0.25	K	±100	10~1M	E24, 96	F(±1%)
		Ctandard	±200	1~9.76		
		Standard	±200	1~22M	E24	J(±5%)

Style	Limiting element voltage (V)	Insulation voltage (V)	Category temperature range (°C)
RMCU16	75	100	-55 ~ +155
RMCU20	150	500	-55~+155

3.3 Derating

The derated values of dissipation at temperature in excess of 70 °C shall be as indicated by the following curve.

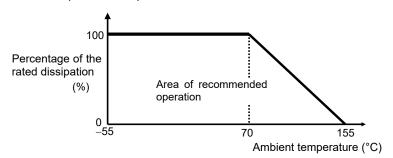


Figure-1 Derating curve

3.4 Rated voltage

d. c. or a. c. r. m. s. voltage calculated from the square root of the product of the rated resistance and the rated dissipation.

E : Rated voltage (V)

P : Rated dissipation (W)

R : Rated resistance
$$(\Omega)$$

Limiting element voltage can only be applied to resistors when the resistance value is equal to or higher than the critical resistance value.

At high value of resistance, the rated voltage may not be applicable.

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4. Packaging form

The standard packaging form shall be in accordance with Table-2.

Table-2

Symbol	pol Packaging form		Standard packaging quantity / units	Application
TP	Paper taping	8mm width, 4mm pitches	5,000 pcs.	RMCU16, 20

5. Dimensions

5.1 The resistor shall be of the design and physical dimensions in accordance with Figure-2 and Table-3.

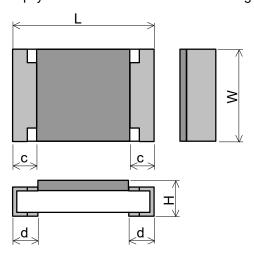


Figure-2

Table-3

Unit: mm

Style	L	W	Н	С	d
RMCU16	1.6±0.1	0.8 ^{+0.15}	0.45±0.10	0.3±0.1	0.3±0.1
RMCU20	2.0±0.1	1.25±0.10	0.55±0.10	0.4±0.2	0.4±0.2

5.2 Net weight (Reference)

= 1 tot 110.g. 1. (1 to 10101100)		
Style	Net weight(mg)	
RMCU16	2	
RMCU20	5	



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6. Marking

6.1 RMCU20

The nominal resistance shall be marked in 3 digits or 4 digits and marked on over coat side.

• E24 series: 3 digits, E96 series: 4 digits

In case of the resistance value that E96 overlaps with E24, It is marked by either.

		,
Marking example	Contents	Application
123	12×10 ³ $[\Omega] \rightarrow$ 12 $[k\Omega]$	RMCU20
2.2	2.2 [Ω]	Less than 10Ω of RMCU20
5623	$562\times10^{3} [\Omega] \rightarrow 562[k\Omega]$	RMCU20
12R7	12.7 [Ω]	RMCU20

6.2 RMCU16

The nominal resistance shall be marked in 3 digits and marked on over coat side.

No marking in the E96 series.

Marking example	Contents	Application
123	12×10 ³ $[\Omega] \rightarrow$ 12 $[k\Omega]$	E24
2R2	2.2 [Ω]	E24
No marking	$102\times10^2 \left[\Omega\right] \rightarrow 10.2 \left[k\Omega\right]$	E96
No marking	332×10^{-1} [Ω] $\rightarrow 33.2$ [Ω]	E96

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7. Performance

7.1 The standard condition for tests shall be in accordance with Sub-clause 4.2, JIS C 5201–1: 2011.

7.2 The performance shall be satisfied in Table-4.

Table-4(1)

		Tabl e 4 (T)	
No	Test items	Condition of test	Performance requirements
1	High temperature exposure AEC Q200 - No.3	MIL-STD-202 Method 108 Ambient temperature:155±2°C, Condition: Without load, Duration: 1000 +4000 h Interval measurements: 250 h and 500 h	Δ R/R: Within \pm (2%+0.1 Ω) No visible damage
2	Temperature cycling AEC Q200 - No.4	JESD22 Method JA-104 Temperature: -55±3°C / 125±2°C, Dwell time: 30min maximum at each temp. Transition time: 1 min. max. Number of cycles: 1000 cycles. Interval measurements: 250 cy and 500 cy	Δ R/R: Within ±(1%+0.05Ω) No visible damage
3	Bias humidity AEC Q200 – No.7	MIL-STD-202 Method 103 Condition: 85°C & 85% R.H. Test power: 10% of rated power shall be applied for continuously. Duration: 1,000 ⁺⁴⁸ ₀ h Interval measurements: 250 h and 500 h	Δ R/R: Within \pm (2%+0.1 Ω) No visible damage
4	Operational life AEC Q200 – No.8	MIL-STD-202 Method 108 Ambient temperature: 125±2°C The applied voltage shall be the voltage to be calculated at 35% of rated dissipation or the limiting element voltage whichever is the smaller. Condition: The voltage shall be applied for continuously. Duration: 1000 +48	ΔR/R: Within ±(2%+0.1Ω) No visible damage
5	External Visual AEC Q200 – No.9	MIL-STD-883 Method 2009	Inspect device construction, marking and workmanship.
6	Dimensions AEC Q200 – No.10	JESD22 Method JB-100	As in Table-3
7	Resistance to Solvents AEC Q200 – No.12	MIL-STD-202 Method 215 Solvent: 2-propanol at 25°C Immersion time: 3 min Brush: 10 times brushing Immersion and brush cycle: 3cycle	Δ R/R: Within \pm (1%+0.05 Ω) No visible damage
8	Mechanical Shock AEC Q200 – No.13	MIL-STD-202 Method 213 Waveform: half sine, Peak value100G, Normal duration 6ms Condition: XX'YY'ZZ', 18times total	Δ R/R: Within $\pm (0.5\% + 0.05\Omega)$ No visible damage

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Table-4(2)

		Iable-4(2)		
No	Test items	Condition of test	Performance requirements	
9	Vibration	MIL-STD-202 Method 204	Δ R/R: Within \pm (0.5%+0.05 Ω)	
	AEC Q200 – No.14	Peak acceleration and Sweep time: 5 g's for 20	No visible damage	
		min , Frequency 10Hz to 2000Hz,	_	
		Condition: 12 cycles each of 3 orientations		
10	Resistance to soldering heat	MIL-STD-202 Method 210	Δ R/R: Within \pm (0.5%+0.05 Ω)	
	AEC Q200 - No.15	Solder bath temp: 260±5°C	No visible damage	
		Immersed time: 10±1s		
11	ESD test	AEC-Q200-002	Δ R/R: Within \pm (5%+0.1 Ω)	
	AEC Q200 – No.17	Human body model, 2 Kohm, 150 pF,	No visible damage	
		Test voltage: 2000V		
12	Solderability	J-STD-002	The surface of terminal immersed shall	
	AEC Q200 – No.18	a) Bake the sample for 155 °C dwell time 4h /	be min. of 95% covered with a new	
		solder dipping 235°C/ 5s.	coating of solder.	
		Solder: Sn96.5-Ag3-Cu0.5		
		b) Category 3, Solder dipping 215°C/ 5s.		
		Solder: Sn63Pb37		
		c) Category 3, Solder dipping 260°C/ 30s.		
13	Electrical Characterization	1. D.C. Resistance	The resistance value shall	
	AEC Q200 - No.19	2. Temperature Coefficient of Resistance	correspond with the rated resistance	
		-55 °C / +20°C	taking into account the specified	
		+20 °C / +155°C	tolerance.	
			2. As in Table–1	
14	Flammability	UL-94	V-0 or V-1 are acceptable	
	AEC Q200 - No.20			
15	Bending strength	AEC-Q200-005	Δ R/R: Within $\pm (0.5\% + 0.05\Omega)$	
	AEC Q200 – No.21	Bending value2mm	No visible damage	
		Holding time: 60sec.		
		AEC-Q200-006	Δ R/R: Within \pm (0.5%+0.05 Ω)	
	AEC Q200 – No.22	Pressurizing force:	No visible damage	
		RMCU20 17.7N		
		RMCU16 10N		
47 5		Test time: 60±1s.	T. C. FANLING ODITION	
17	Flame retardance	AEC-Q200-001	The following FAILURE CRITERIA	
	AEC Q200 – No.24	Test conditions: 9VDC to 32VDC Each 1h	does not occur.	
		This test applies to rated voltages of 32V and	FAILURE CRITERIA	
		above.	- Electrically open	
			a) A flame over 3.0 seconds duration	
			b) An explosion	
			c) A temperature above 350°C	
			sustained for over 10 s	

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8. Taping

8.1 Applicable documents JIS C 0806-3: 2014, EIAJ ET-7200C: 2010

8.2 Taping dimensions

Paper taping (8mm width, 4mm pitches)

Taping dimensions shall be in accordance with Figure-3 and Table-5.

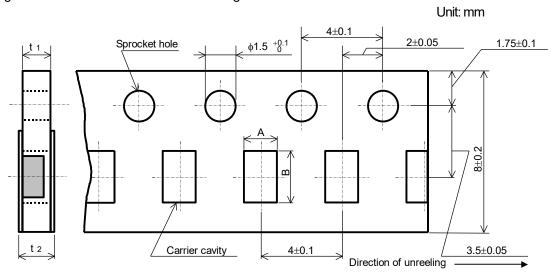


Figure-3

	Unit: mm			
Style	Α	В	t 1	t 2
RMCU16	1.15±0.15	1.9±0.2	0.6±0.1	0.8max.
RMCU20	1.65±0.15	2.5±0.2	0.8±0.1	1.0max.

- 1). The cover tapes shall not cover the sprocket holes.
- Tapes in adjacent layers shall not stick together in the packing.
- 3). Components shall not stick to the carrier tape or to the cover tape.
- 4). Pitch tolerance over any 10 pitches ±0.2mm.
- 5). The peel strength of the top cover tape shall be with in 0.1N to 0.5N on the test method as shown in the following Figure-4.
- 6). When the tape is bent with the minimum radius for 25 mm the tape shall not be damaged and the components shall maintain their position and orientation in the tape.
- 7). In no case shall there be two or more consecutive components missing. The maximum number of missing components shall be one or 0.1%, whichever is greater.
- 8). The resistors shall be faced to upward at the over coating side in the carrier cavity.

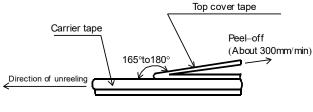


Figure-4

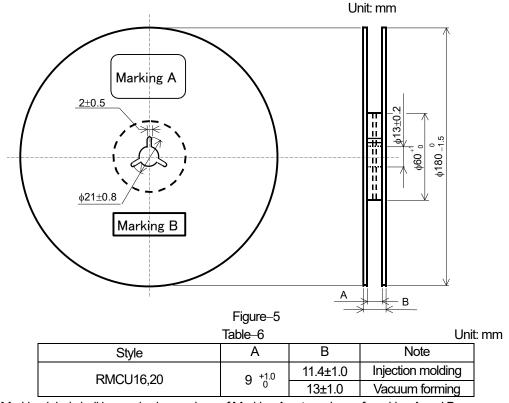
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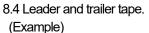
8.3 Reel dimension

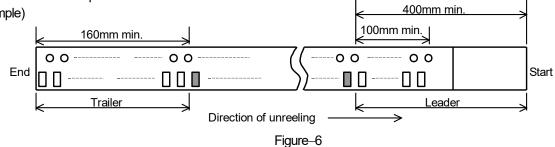
Reel dimensions shall be in accordance with the following Figure–5 and Table–6.

Plastic reel (Based on EIAJ ET-7200C)



Note: Marking label shall be marked on a place of Marking A or two place of marking A and B.





9. Marking on package

The label of a minimum package shall be legibly marked with follows.

9.1 Marking A

(1) Classification

(Style, Temperature coefficient of resistance, Rated resistance, Tolerance on rated resistance, Packaging form)

(2) Quantity (3) Lot number (4) Manufacturer's name or trade mark (5) Others

9.2 Marking B (KAMAYA Control label)