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	No.:	SBF-K-HTS-0001 /5
	Date:	2023. 1. 25
Data s	heet	
Title: CHIP FUSE; RECTANG	ULAR TYP	E
Style: SBF32[Optional code:A	S]	
	year from shipme bility shall be satis I in this data she	en t by the company. sfied. eet are subject to change at any cification for any quality
	S Z A MAYA E	發株式會社 LECTRIC CO., LTD. Hokkaido Research Center Approval by: T. Sannomiya Drawing by: M. Shibuya

Title:	CHIP FUSES; RECTANGULAR TYPE	
	SBF32[Optional code: AS]	

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1. Scope

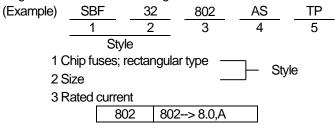
1.1 This data sheet covers the detail requirements for chip fuses; rectangular type, style of SBF32 [Optional code: AS].

1.2 Applicable documents

UL248–1–2000 Low–Voltage Fuses–Part1: General Requirements UL248–14–2000 Low–Voltage Fuses–Part14: Supplemental Fuses CSA C22.2 No.248.1–2000 Low–Voltage Fuses–Part1: General Requirements CSA C22.2 No.248.14–2000 Low–Voltage Fuses–Part14: Supplemental Fuses

2. Classification

Type designation shall be the following form.



4 Optional code

Symbol	Optional code
AS	Standard

5 Packaging form

0.0	
В	Bulk (loose package)
TP	Paper taping

3. Safety standard approval

• UL248-1 and UL248-14

• CSA C22.2, No. 248.1–00 and CSA C22.2, No. 248.14–00 The file number to be designated by UL and C–UL shall be as follows: E176847

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4. Rating

4.1 The ratings shall be in accordance with Table-1.

				-	Table–1					
Style	Rated current		Internal	Rated	Breaking	Time / current characteristic				
	Symbol	(A)	Marking symbol	resistance value (mΩ Typ.)	resistance	capacity (A)	Current	Pre-arcing time		
	102	1.0	S10	130						
	132	1.25	S13	94	63					
	152	1.5	S15	68	32					
	202	2.0	S20	40						
	252	2.5	S25	30					100%	4h min.
SBF32	302	3.0	S30	24			50	200%	1~120s max	
001 02	402	4.0	S40	15			00	300%	0.02s~3.0s max	
	502	5.0	S50	12			800%	0.0015s~0.05s max		
	602	6.0	S60	10						
	702	7.0	S70	7						
	802	8.0	S80	6						

Style	Working temperature range(°C)
SBF32	-55 to +125

5. Packaging form

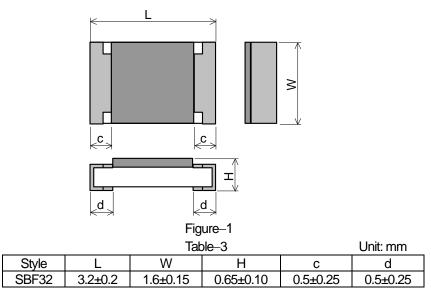
The standard packaging form shall be in accordance with Table-2.

Table-2

Symbol	Packaging form		Standard packaging quantity / units
В	Bulk (loose package)		1,000 pcs.
TP	Paper taping	8mm width, 4mm pitches	5,000 pcs.

6. Dimensions

6.1 The resistor shall be of the design and physical dimensions in accordance with Figure-1 and Table-3.



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6.2 Net weight (Reference)

Style	Net weight(mg)	
SBF32	10	

7. Marking

The Marking symbol of Sub- clause 4.1 shall be marked on over coat side. (Example) "S80" \rightarrow Content: SBF32 802 AS

8. Performance

8.1 Unless otherwise specified, the standard range of atmospheric conditions for tests is as follows; Ambient temperature: 5 °C to 35 °C, Relative humidity: 45 % to 85 %, Air presser: 86 kPa to 106 kPa If there is any doubt the results, measurements shall be made within the following: Ambient temperature: 20 °C ± 2 °C, Relative humidity: 60 % to 70 %, Air presser: 86 kPa to 106 kPa

_ . .

8.2 The performance shall be satisfied in Table-4.

		Table-4(1)		
No.	Test items	Condition of test	Performa	ance requirements
1	Temperature rise	The fuse shall be mounted on the test substrate as shown in Figure–2. Measurement temp.: 10 °C to 30 °C Test current: Rated current The temperature at the hottest point on the surface	75 ℃ max.	
		of the fuse shall be measured after temperature		
2	Time / current characteristic	equilibrium has been attained. The fuse shall be mounted on the test substrate as	Current	Pre-arcing time
2	Time / current characteristic	shown in Figure-2.	100%	4h min.
		Test current shall be applied for continuously.	200%	1~120s max
			300%	0.02s~3.0s max
			800%	0.0015s~0.05s max
3	Terminal bond strength of the face plating	<u>JIS C 60068-2-21 Ue1</u> The fuse shall be mounted on the test substrate as shown in Figure–2. Bending value: 3 mm(Among the fulcrums: 90 mm) Duration: 10 s \pm 1 s	Change of in ±10% No eviden damage.	ternal resistance: ce of mechanical
4	Resistance to soldering heat	Test by a piece. Temp. of solder bath: $260 \degree C \pm 5 \degree C$ Immersion time: $10 \ s \pm 1 \ s$ After immersion into solder, leaving the room temp. for 1h or more, and then measure the internal resistance. • Reflow soldering Pre-heating: $150 \degree C \sim 180 \degree C$, $120 \ s$ max. Peak: $260 \degree C \pm 5 \degree C$, $10 \ s$ max. Reflow cycle: 2 times	Change of in ±10% No eviden damage	ternal resistance: ce of appearance
5	Solderability	After immersion into solder, leaving the room temp. for 1h or more, and then measure the internal resistance. $\frac{JIS C 60068-2-58}{Test by a piece}$ Flux: Rosin–Methanol Temp. of solder: bath: 235 °C ± 5 °C Immersion time: 2 s ± 0.5 s		of terminal immersed of 95 % covered with g of solder.

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		表一4(2)	
No.	Test items	Condition of test	Performance requirements
6	Rapid change temperature	JIS C 60068-2-14 Na	Change of internal resistance:
		The fuse shall be mounted on the test substrate	±10%
		as shown in Figure–2.	No evidence of appearance
		Lower temperature: –55 °C	damage
		Upper temperature: +125 °C	
		Duration of exposure at each temperature: 30	
		min.	
		Number of cycles: 5 cycles	

9. Test substrate

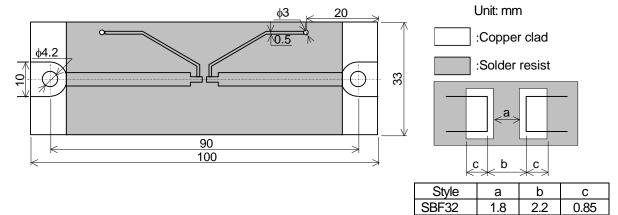


Figure-2 SBF TEST SUBSTRATE

Remark 1). Material: Epoxide woven glass Thickness: 1. 6mm Thickness of copper clad: 0. 035mm

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10. Taping

10.1 Applicable documents JIS C 0806-3: 2014, EIAJ ET-7200C: 2010

10.2 Taping dimensions

Paper taping (8mm width, 4mm pitches)

Taping dimensions shall be in accordance with Figure-3 and Table-5.

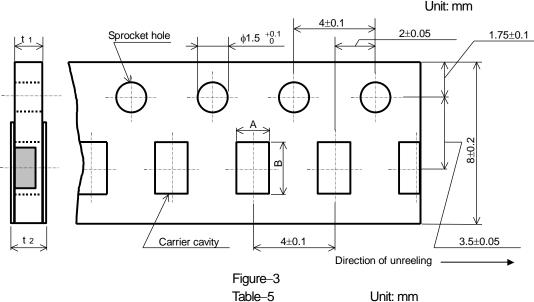


Table-5				Unit: mm
Style	A	В	t 1	t 2
SBF32	2.0 <u>+</u> 0.15	3.6±0.2	0.8 <u>+</u> 0.1	1.0max.

- 1). The cover tapes shall not cover the sprocket holes.
- 2). Tapes in adjacent layers shall not stick together in the packing.
- 3). Components shall not stick to the carrier tape or to the cover tape.
- 4). Pitch tolerance over any 10 pitches ±0.2mm.
- 5). The peel strength of the top cover tape shall be with in 0.1N to 0.5N on the test method as shown in the following Figure-4.
- 6). When the tape is bent with the minimum radius for 25 mm, the tape shall not be damaged and the components shall maintain their position and orientation in the tape.
- 7). In no case shall there be two or more consecutive components missing. The maximum number of missing components shall be one or 0.1%, whichever is greater.
- 8). The fuses shall be faced to upward at the over coating side in the carrier cavity.

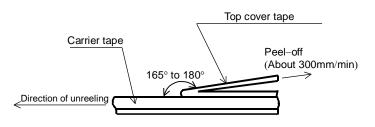


Figure-4

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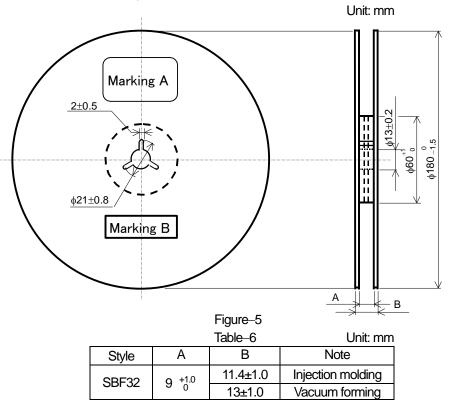
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10.3 Reel dimension

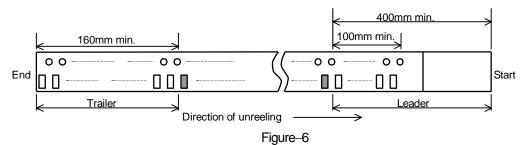
Reel dimensions shall be in accordance with the following Figure-5 and Table-6.

Plastic reel (Based on EIAJ ET-7200C)



Note: Marking label shall be marked on a place of Marking A or two place of marking A and B.

10.4 Leader and trailer tape.



11. Marking on package

The label of a minimum package shall be legibly marked with follows.

11.1 Marking A

(1) Classification (Style, Rated current, Optional code, Packaging form) (2) Quantity (3) Lot number

(4) Manufacturer's name or trade mark (5) UL and /or C–UL recognized component mark (6) Others

11.2 Marking B (KAMAYA Control label)

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12. Recommended Derating for Rated Current

This fuse will recommend use by the current reduction value according to the following derating curve.

Nominal Derating
Nominal Derating < 759

Nominal Derating \leq 75% of Rated Current

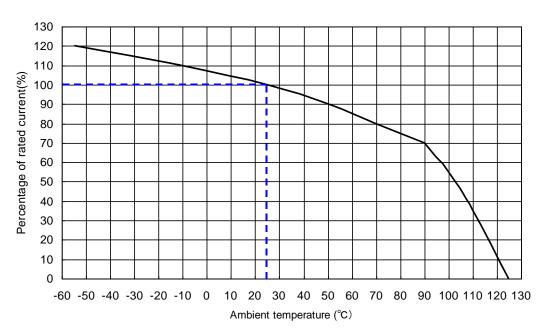
• Temperature Derating

Please refer to the following graph regarding the current derating value for ambient temperature.

Ex.) If SBF32 801 (Rated Current 8.0A) is used under ambient temperature 70°C,

Kamaya recommends, less than the current value derated as below,

Rated Current : $8.0A \times$ (Nominal Derating : $75\% \times$ Temperature Derating : 80%) = 4.8A



Derating curve

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